

Day : Saturday
Date: 9/16/2006


PALM INTRANET

Time: 16:15:44

Inventor Name Search Result

Your Search was:

Last Name = WU

First Name = BIING-SENG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07389227	5017983	150	08/03/1989	AMORPHOUS SILICON THIN FILM TRANSISTOR WITH A DEPLETION GATE	WU, BIING-SENG
07392743	Not Issued	164	08/10/1989	INVERTED COPLANAR AMORPHOUS SILICON THIN FILM TRANSISTOR	WU, BIING-SENG
07480475	5053347	150	02/14/1990	AMORPHOUS SILICON THIN FILM TRANSISTOR WITH A DEPLETION GATE	WU, BIING-SENG
07491679	Not Issued	166	03/12/1990	INVERTED COPLANAR AMORPHOUS SILICON THIN FILM TRANSISTOR	WU, BIING-SENG
07495829	Not Issued	161	03/19/1990	PROCESS FOR MAKING A SELF-ALIGNED INVERTED COPLANAR/STAGGERED AMORPHOUS SILICON THIN FILM TRANSISTOR	WU, BIING-SENG
07558772	5075237	150	07/26/1990	PROCESS OF MAKING A HIGH PHOTSENSITIVE DEPLETION-GATE THIN FILM TRANSISTOR	WU, BIING-SENG
07674451	5196911	150	03/25/1991	HIGH PHOTSENSITIVE DEPLETION-GATE THIN FILM TRANSISTOR	WU, BIING-SENG
07783575	5193018	150	10/28/1991	ACTIVE MATRIX LIQUID CRYSTAL DISPLAY SYSTEM USING COMPLEMENTARY THIN FILM TRANSISTORS	WU, BIING-SENG
07826674	5173753	150	01/27/1992	INVERTED COPLANAR AMORPHOUS SILICON THIN FILM TRANSISTOR WHICH PROVIDES SMALL CONTACT CAPACITANCE AND RESISTANCE	WU, BIING-SENG

<u>07860430</u>	<u>5285302</u>	150	03/30/1992	TFT MATRIX LIQUID CRYSTAL DISPLAY WITH COMPENSATION CAPACITANCE PLUS TFT STRAY CAPACITANCE CONSTANT IRRESPECTIVE OF MASK MISALIGNMENT DURING PATTERNING	WU, BIING-SENG
<u>07875651</u>	Not Issued	166	03/01/1994	STRUCTURE AND METHOD OF MANUFACTURING A THIN FILM TRANSISTOR	WU, BIING-SENG
<u>07877711</u>	<u>5233448</u>	150	05/04/1992	METHOD OF MANUFACTURING A LIQUID CRYSTAL DISPLAY PANEL INCLUDING PHOTOCONDUCTIVE ELECTROSTATIC PROTECTION	WU, BIING-SENG
<u>07881076</u>	<u>5260818</u>	150	05/11/1992	DISPLAY PANEL PROVIDED WITH REPAIR CAPABILITY OF DEFECTIVE ELEMENTS AND METHOD OF REPAIR	WU, BIING-SENG
<u>08005794</u>	<u>5355002</u>	150	01/19/1993	STRUCTURE OF HIGH YIELD THIN FILM TRANSISTORS	WU, BIING-SENG
<u>08052519</u>	<u>5441905</u>	150	04/29/1993	PROCESS OF MAKING SELF-ALIGNED AMORPHOUS-SILICON THIN FILM TRANSISTORS	WU, BIING-SENG
<u>08084484</u>	<u>5396150</u>	150	07/01/1993	SINGLE TIP REDUNDANCY METHOD AND RESULTING FLAT PANEL DISPLAY	WU, BIING-SENG
<u>08747503</u>	<u>5721164</u>	150	11/12/1996	METHOD OF MANUFACTURING THIN FILM TRANSISTORS	WU, BIING-SENG
<u>08810094</u>	<u>5828082</u>	150	03/03/1997	THIN FILM TRANSISTOR HAVING DUAL INSULATION LAYER WITH A WINDOW ABOVE GATE ELECTRODE	WU, BIING-SENG
<u>08982004</u>	<u>6265249</u>	150	12/01/1997	STRUCTURE AND METHOD OF MANUFACTURING THIN FILM TRANSISTORS	WU, BIING-SENG
<u>09317133</u>	<u>6184069</u>	150	05/24/1999	THIN FILM TRANSISTOR-LIQUID CRYSTAL DISPLAY WITH SELF-ALIGNED TRANSPARENT CONDUCTING LAYERS	WU, BIING-SENG
<u>09317134</u>	<u>6261880</u>	150	05/24/1999	PROCESS FOR MANUFACTURING THIN FILM	WU, BIING-SENG

				TRANSISTORS	
<u>09368515</u>	<u>6313899</u>	150	08/05/1999	WIDE VIEWING ANGLE LIQUID CRYSTAL DISPLAY	WU, BIING-SENG
<u>09384412</u>	<u>6317113</u>	150	08/27/1999	METHOD FOR DRIVING THIN FILM TRANSISTOR OF LIQUID CRYSTAL DISPLAY	WU, BIING-SENG
<u>09609651</u>	<u>6943781</u>	150	07/03/2000	LIQUID CRYSTAL DISPLAY MODULE AND ITS SCANNING CIRCUIT BOARD	WU, BIING-SENG
<u>09705231</u>	<u>6683660</u>	150	11/01/2000	ACTIVE MATRIX LIQUID CRYSTAL DISPLAY AND METHOD OF MAKING THE SAME	WU, BIING-SENG
<u>09767340</u>	<u>6515301</u>	150	01/23/2001	STRUCTURE AND FABRICATION METHOD OF FLAT PANEL DISPLAY COMPRISING ADDRESS LINE WITH MENDING LAYER	WU, BIING-SENG
<u>09789591</u>	<u>6753935</u>	150	02/22/2001	LIQUID CRYSTAL DISPLAY ELEMENT WITH A DEFECT REPAIRING FUNCTION	WU, BIING-SENG
<u>09826096</u>	Not Issued	71	04/05/2001	Method of reducing flickering and inhomogeneous brightness in LCD	WU, BIING-SENG
<u>10026712</u>	<u>6710757</u>	150	12/27/2001	METHOD OF DRIVING AN ACTIVE MATRIX ELECTRO-LUMINESCENT DISPLAY	WU, BIING-SENG
<u>10247553</u>	<u>6861749</u>	150	09/20/2002	A SEMICONDUCTOR DEVICE WITH BUMP ELECTRODES	WU, BIING-SENG
<u>10310589</u>	<u>6712666</u>	150	12/04/2002	FABRICATION METHOD OF FLAT PANEL DISPLAY COMPRISING ADDRESS LINE WITH MENDING LAYER	WU, BIING-SENG
<u>10848067</u>	<u>7098989</u>	150	05/19/2004	LIQUID CRYSTAL DISPLAY ELEMENT WITH A DEFECT REPAIRING FUNCTION	WU, BIING-SENG
<u>11193399</u>	Not Issued	30	08/01/2005	Color-sequential display method	WU, BIING-SENG
<u>11201533</u>	Not Issued	20	08/11/2005	Liquid crystal display module and scanning circuit board thereof	WU, BIING-SENG
<u>11403053</u>	Not Issued	25	04/11/2006	WOA panel architecture	WU, BIING-SENG
<u>11475529</u>	Not Issued	19	06/27/2006	Planar light source device	WU, BIING-SENG
<u>11492080</u>	Not Issued	30	07/25/2006	Display device and method of manufacturing the same	WU, BIING-SENG

60671355	Not Issued	159	04/14/2005	WOA panel architecture	WU, BIING-SENG
60840706	Not Issued	20	08/29/2006	Liquid crystal display and methods for driving the same	WU, BIING-SENG

Inventor Search Completed: No Records to Display.

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	<input type="text" value="WU"/>	<input type="text" value="BIING-SENG"/>	

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Day : Saturday
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Inventor Name Search Result

Your Search was:

Last Name = SAH

First Name = WEN-JYH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07906737	5351309	150	06/30/1992	IMAGE EDGE SENSOR	SAH, WEN-JYH
08135284	Not Issued	161	10/12/1993	IMAGE EDGE SENSOR	SAH, WEN-JYH
08135897	Not Issued	166	10/13/1993	IMAGE EDGE SENSOR	SAH, WEN-JYH
08359957	Not Issued	161	12/20/1994	IMAGE EDGE SENSOR	SAH, WEN-JYH
08760101	6175394	150	12/03/1996	CAPACITIVELY COUPLED FIELD EFFECT TRANSISTORS FOR ELECTROSTATIC DISCHARGE PROTECTION IN FLAT PANEL DISPLAYS	SAH, WEN-JYH
09313073	Not Issued	161	05/17/1999	METHOD FOR FORMING A THIN FILM TRANSISTOR OF LIQUID CRYSTAL DISPLAY	SAH, WEN-JYH
09321210	6218221	150	05/27/1999	THIN FILM TRANSISTOR WITH A MULTI-METAL STRUCTURE AND A METHOD OF MANUFACTURING THE SAME	SAH, WEN-JYH
09321211	Not Issued	161	05/27/1999	METHOD FOR FORMING ITO/METAL CONTACT IN TOP ITO TFT PROCESS	SAH, WEN-JYH
09325771	6281701	150	06/04/1999	APPARATUS FOR TESTING FLAT PANEL DISPLAY	SAH, WEN-JYH
09328580	6104042	150	06/10/1999	THIN FILM TRANSISTOR WITH A MULTI-METAL STRUCTURE A METHOD OF MANUFACTURING THE SAME	SAH, WEN-JYH
09351256	6297161	150	07/12/1999	METHOD FOR FORMING TFT ARRAY BUS	SAH, WEN-JYH

<u>09826096</u>	Not Issued	71	04/05/2001	Method of reducing flickering and inhomogeneous brightness in LCD	SAH, WEN-JYH
<u>10249671</u>	<u>6819370</u>	150	04/29/2003	LIQUID CRYSTAL DISPLAY PANEL	SAH, WEN-JYH
<u>10463553</u>	<u>7034905</u>	150	06/18/2003	liquid crystal display panel having a repair line positioned only underneath the second portion of the non-display area	SAH, WEN-JYH
<u>10750360</u>	Not Issued	61	12/31/2003	Transflective thin film transistor liquid crystal display panel and manufacturing method thereof	SAH, WEN-JYH
<u>60009471</u>	Not Issued	159	12/28/1995	CAPACITIVELY COUPLED FIELD EFFECT TRANSISTORS FOR ELECTROSTATIC DISCHARGE PROTECTION IN FLAT PANEL DISPLAYS	SAH, WEN-JYH

Inventor Search Completed: No Records to Display.

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Day : Saturday
Date: 9/16/2006

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 **PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = WU

First Name = CHAO-WEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09826096	Not Issued	71	04/05/2001	Method of reducing flickering and inhomogeneous brightness in LCD	WU, CHAO-WEN
10064049	6982775	150	06/04/2002	LIQUID CRYSTAL DISPLAY HAVING REDUCED FLICKER	WU, CHAO-WEN
10064107	6809718	150	06/12/2002	TFT-LCD CAPABLE OF ADJUSTING ITS LIGHT SOURCE	WU, CHAO-WEN
10401443	6982690	150	03/27/2003	DISPLAY APPARATUS WITH A DRIVING CIRCUIT IN WHICH EVERY THREE ADJACENT PIXELS ARE COUPLED TO THE SAME DATA LINE	WU, CHAO-WEN

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name
<input type="text" value="WU"/>	<input type="text" value="CHAO-WEN"/>
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EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	12	resistor near (scan\$6 or row) near line	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/16 15:14
L2	211	(resistor or deformat\$5) near2 (scan or row or gate) near2 line\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/16 15:15
L3	51	2 same capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/16 15:29
L4	0	TFT near3 capacitor near3 resistor near3 (scan\$4 or row or gate)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/16 15:30
L5	22	TFT with capacitor with resistor with (scan\$4 or row or gate)with line	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/16 15:30
S1	1310	345/92,98,99,100.ccls.	USPAT	OR	OFF	2006/09/16 15:10
S2	647	TFT and 345/92,98,99,100.ccls.	USPAT	OR	OFF	2002/12/11 13:11
S3	117	(TFT and 345/92,98,99,100.ccls.) and (resistor\$1) and capacitor\$1	USPAT	OR	OFF	2002/12/11 14:18
S4	187	345/90.ccls.	USPAT	OR	OFF	2002/12/11 14:18
S5	98	345/90.ccls. and TFT	USPAT	OR	OFF	2002/12/11 14:18
S6	21	(345/90.ccls. and TFT) and capacitor\$1 and resistor\$1	USPAT	OR	OFF	2002/12/11 14:23
S7	132	TFT same capacitor\$1 same resistor\$1	USPAT	OR	OFF	2002/12/11 14:45
S8	338	TFT and capacitors and resistors	USPAT	OR	OFF	2002/12/11 14:45
S9	110	"345"/\$.ccls. and (TFT and capacitors and resistors)	USPAT	OR	OFF	2002/12/11 14:45
S10	0	scan same (flicker or cross adj talk)same resistor same capacitor and TFT	USPAT	OR	OFF	2002/12/11 15:06
S11	1	(flicker or cross adj talk)same resistor same capacitor and TFT	USPAT	OR	OFF	2002/12/11 15:06

EAST Search History

S12	366	TFT and (flicker or cross adh talk) and capacitor and resistor	USPAT	OR	OFF	2002/12/11 15:07
S13	79	TFT and (flicker or cross adj talk) and capacitor and resistor	USPAT	OR	OFF	2002/12/11 15:35
S14	30	resistor with scan adj line	USPAT	OR	OFF	2002/12/11 15:37
S15	8	resistor same (scan adj line) and TFT	USPAT	OR	OFF	2005/09/29 09:34
S16	1	resistor with (scan adj line) and TFT	USPAT	OR	OFF	2002/12/11 15:40
S17	2900	TFT adj LCD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/05/28 12:38
S18	0	(resistor\$1 adj gate) and (TFT adj LCD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/05/28 12:41
S19	46	(resistor\$1 with gate) and (TFT adj LCD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/05/28 12:56
S20	2	(resistor\$1 with (gate with scan\$4)) and (TFT adj LCD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/05/28 12:56
S21	2	"6421038".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/05/28 14:14
S22	2	"5534860".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/05/28 14:16
S23	2	resistor with gate with (TFT) same scan\$5 adj line	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/05/28 14:18
S24	7	resistor same gate same (TFT) same (scan\$5 adj line)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/05/28 14:18

EAST Search History

S25	2	gate adj voltage adj deformation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 17:14
S26	3486	TFT same capacitor same storage adj capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 17:14
S27	42	S26 same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 17:19
S28	43	tft same capacitor same register same storage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 17:19
S29	4	reisistor with transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 17:20
S30	549	tft with resistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 17:20
S31	47	S30 same lcd	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 17:25
S32	3543	scan\$6 same (tft or transistor or switch\$6) same resistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 17:26
S33	446	S32 same display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 17:26
S34	336	S33 and @ad<"20000406"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 17:35

EAST Search History

S35	123133	resistor near5 capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 17:36
S36	262	S35 same lcd	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 17:36
S37	192	S36 and @ad<"20010405"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 17:36
S38	32	S37 and "345"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/28 17:36
S39	50830	gate with resistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/29 09:35
S40	35653	S39 same (TFT oe transistor or switch\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/29 09:35
S41	35651	S39 same (TFT or transistor or switch\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/29 09:35
S42	84	S41 same lcd	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/29 09:35
S43	18583	capacitors with resistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/29 09:46
S44	5060	S43 with (TFT or transistor or switch\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/29 09:46

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S45	60	S44 same display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/29 09:46
S46	388038	(TFT near lcd) or (liquid adj crystal adj display) or lcd or TFT-LCD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 08:04
S47	331174	gate with (TFT or switch\$5 or transistor or logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 08:04
S48	29223	S47 with resistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 08:05
S49	120	S46 same S48	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 08:17
S50	6502	TFT-LCD or (tft near lcd)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 08:17
S51	0	S50 same (resistor near2 gate)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 08:17
S52	10840	(resistor near2 gate)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 08:18
S53	115	S52 same display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 08:18
S54	77	S53 same (transistor or TFT or switch or logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/20 08:19